

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,900,503 B2  
DATED : May 31, 2005  
INVENTOR(S) : Chang-bong Oh et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

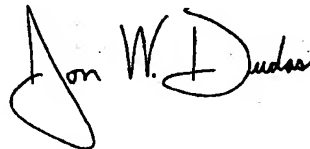
Line 18, delete "dram" and insert -- drain --.

Column 10,

Line 41, after "acute" insert -- angle with a portion of each of the first and second active areas where the NMOS transistors are --.

Signed and Sealed this

Thirtieth Day of August, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

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JON W. DUDAS

*Director of the United States Patent and Trademark Office*